

Abstract

A subsystem comprises a master and a plurality of slaves. The master comprises a clock generator for generating a first clock signal and a second clock signal which have different frequency each other. When command and address signals are received from the master, the plurality of slaves transmits the corresponding data signals to the master. Here, the first clock signal is used to require command and address signals to the corresponding slave, and the second clock signal is used to transmit data signals into the corresponding slave, thereby preserving the improved signal integrity to a command and address buses and simplifying circuit blocks, communication methods and system configuration.